



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Satoshi ETO et al

Serial No.: 09/359,767

Filed: July 22, 1999

For: SEMICONDUCTOR MEMORY DEVICE
AND METHOD FOR EXECUTING SHIFT
REDUNDANCY OPERATION

Allowed: January 24, 2002

Confirmation No.: 3251

Group Art Unit: 2184

Examiner: D. Le

SUBMISSION OF REPLACEMENT DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

March 20, 2007

Sir:

In response to the Examiner's requirement in the Notice of Allowability dated January 24, 2002, enclosed are replacement drawings. It is submitted that these replacement drawings overcome the objections to the originally filed drawings.

In the event that any fees are due with respect to this paper, please charge Deposit Account No. 01-2300, referencing Atty. Docket No. 100021-09026.

Respectfully submitted,

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Atty. Docket No.: 100021-09026

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Enclosure: Replacement Drawings (87 sheets)